

PRASAD V POTLURI SIDDHARTHA INSTITUTE OF TECHNOLOGY

KANURU, VIJAYAWADA

DEPARTMENT OF FRESHMAN ENGINEERING

CIRCULAR

Date: 02.11.24

A program WINGS (Wholesome Integration Nurturing Good Students) is initiated for all the I. B. Tech. students to guide them to the pathway of success in different domains.

As a part of Talk-4 a one day seminar on “**VLSI: Vision Your Roadmap to Semiconductor Opportunities**” is organized to the I B.Tech. ECE Students to create awareness regarding opportunities in VLSI in their respective core for molding their careers.

Topic: “VLSI: Vision Your Roadmap to Semiconductor Opportunities”

Speakers: Chintalapudi Hemanth Surya Manikanta
Senior ASIC Design Engineer at 7 Rays.

Date: 04.11.24

Time: 10.00 am



(Dr.M.SriLakshmi)

Head



Freshman Engineering Department
VP Siddhartha Institute of Technology
Kanuru, VIJAYAWADA-520 007.



PVP SIDDHARTHA INSTITUTE OF TECHNOLOGY

DEPARTMENT OF FRESHMAN ENGINEERING

SUMMARY REPORT ON EVENT ORGANIZED

ACADEMIC YEAR : 2024-2025	
Date of Event organized	4.11.2024
Name of the Event	Wings Talk Series
Resource person	Chintalapudi Hemanth Surya Manikanta Senior ASIC Design Engineer at 7 Rays.
Title of the Event	"VLSI: Vision Your Roadmap to Semiconductor Opportunities"
Brief Report on the Event	A one-day seminar on " VLSI: Vision Your Roadmap to Semiconductor Opportunities " was organized for the I B.Tech.ECE students. The seminar aimed at providing insights into the world of Very Large Scale Integration (VLSI) design and semiconductor opportunities, which are increasingly critical to today's technology-driven economy. The event attracted students & faculty members, eager to learn about the latest trends, career opportunities, and the growing relevance of VLSI in the semiconductor industry.
Year/Semester	I YEAR / I SEM
No. of the participants	130
Consolidated Feedback	Good
Suggestions if any	---
Name of the Co-ordinators	Mrs. T. Krishna Sree, Asst. Prof., FED Dr. M.Silpa, Asst. Prof., FED.
Signature of the Co-ordinators	
Signature of the HOD	

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Siddhartha Institute of Technology
(Autonomous)
Approved by AICTE and Affiliated to JNTU Kakinda
Sponsored By: Siddhartha Academy of General and Technical Education, Vijayawada



EAMCET Code: PPSV



FRESHMAN ENGINEERING DEPARTMENT ORGANIZES

W HOLESOME
I NTEGRATION
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S TUDENTS



TALK SERIES # 4

VLSI VISION YOUR ROADMAP TO
SEMICONDUCTOR OPPORTUNITIES

(04-11-2024)



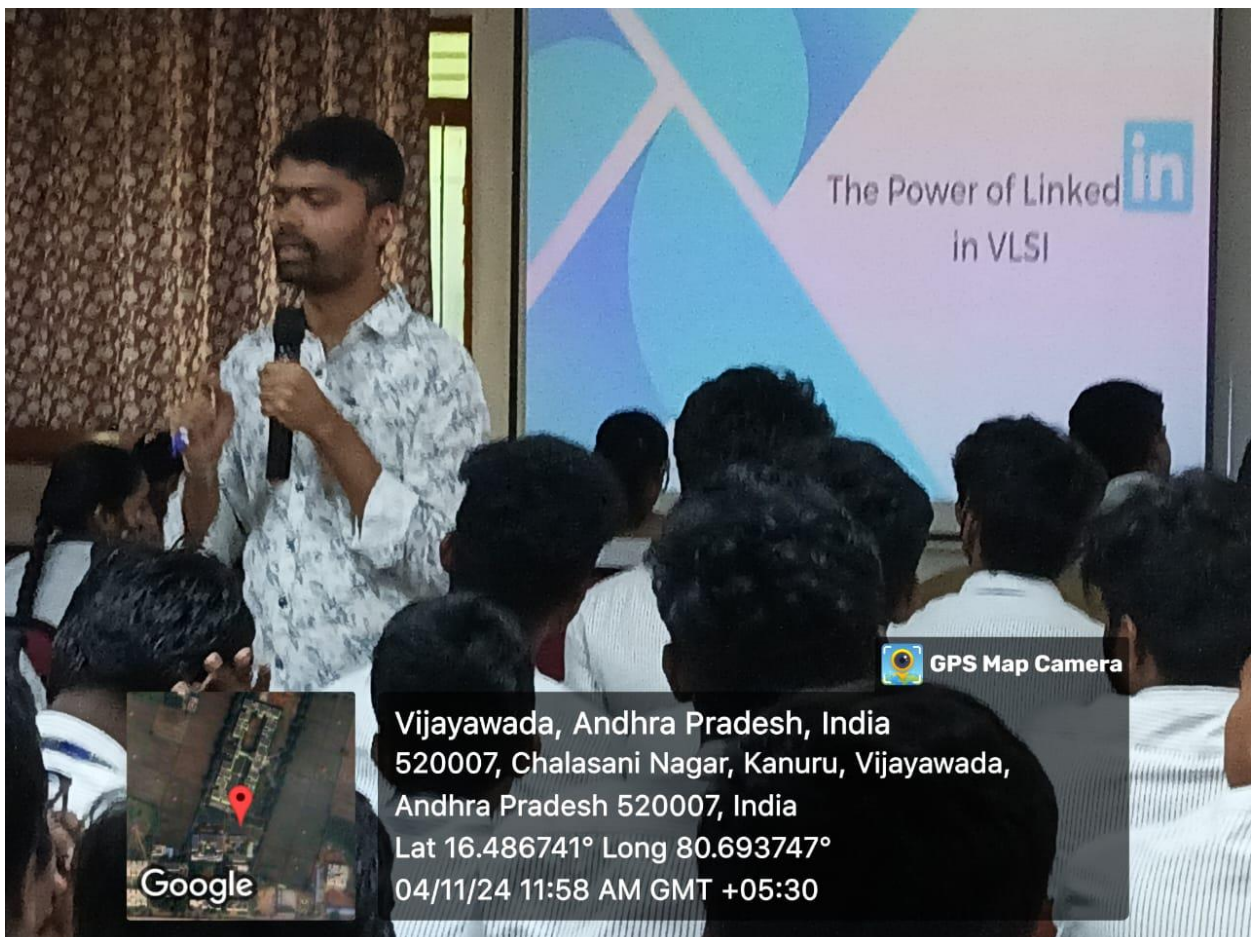
MR. HEMANTH CHINTALAPUDI.
SENIOR ASIC DESIGN ENGINEER AT 7RAYS

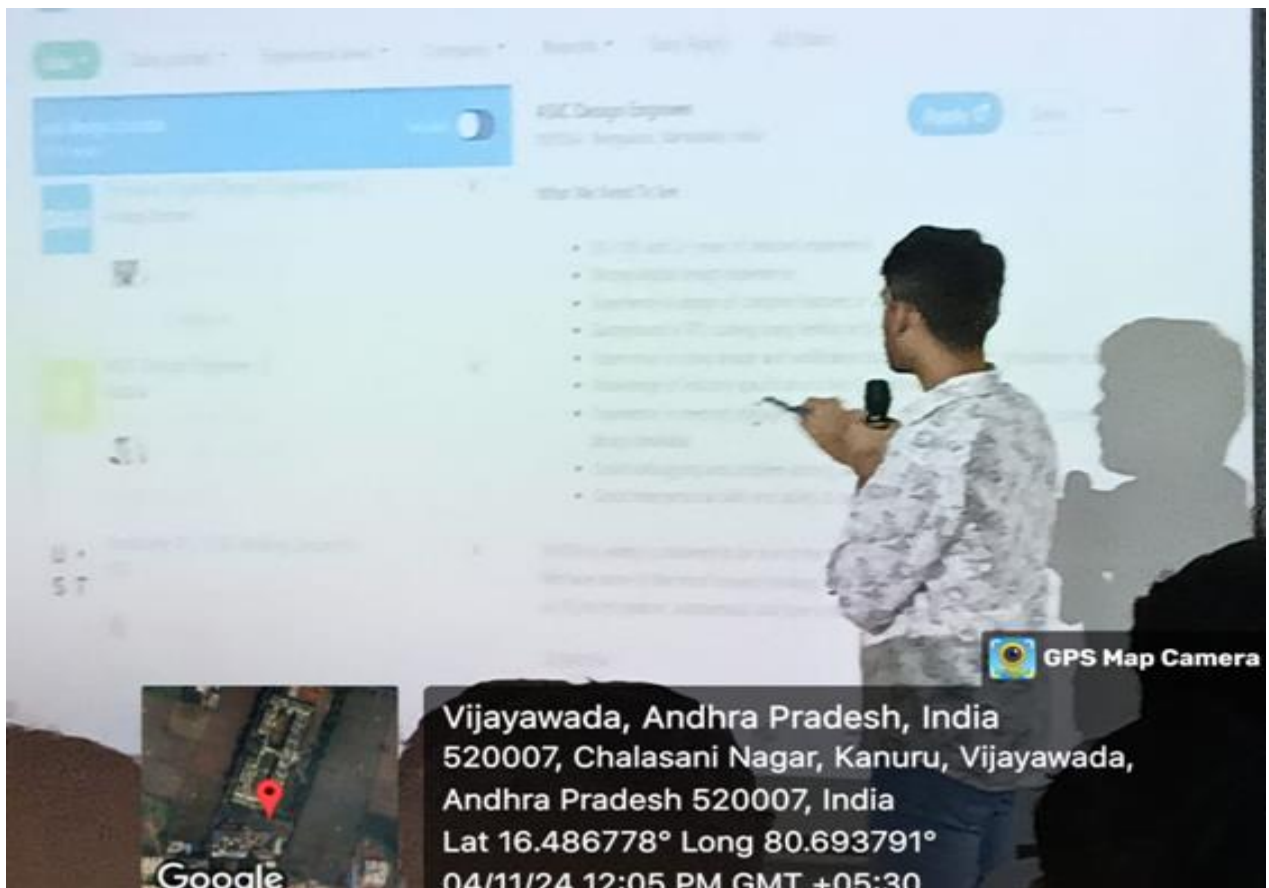


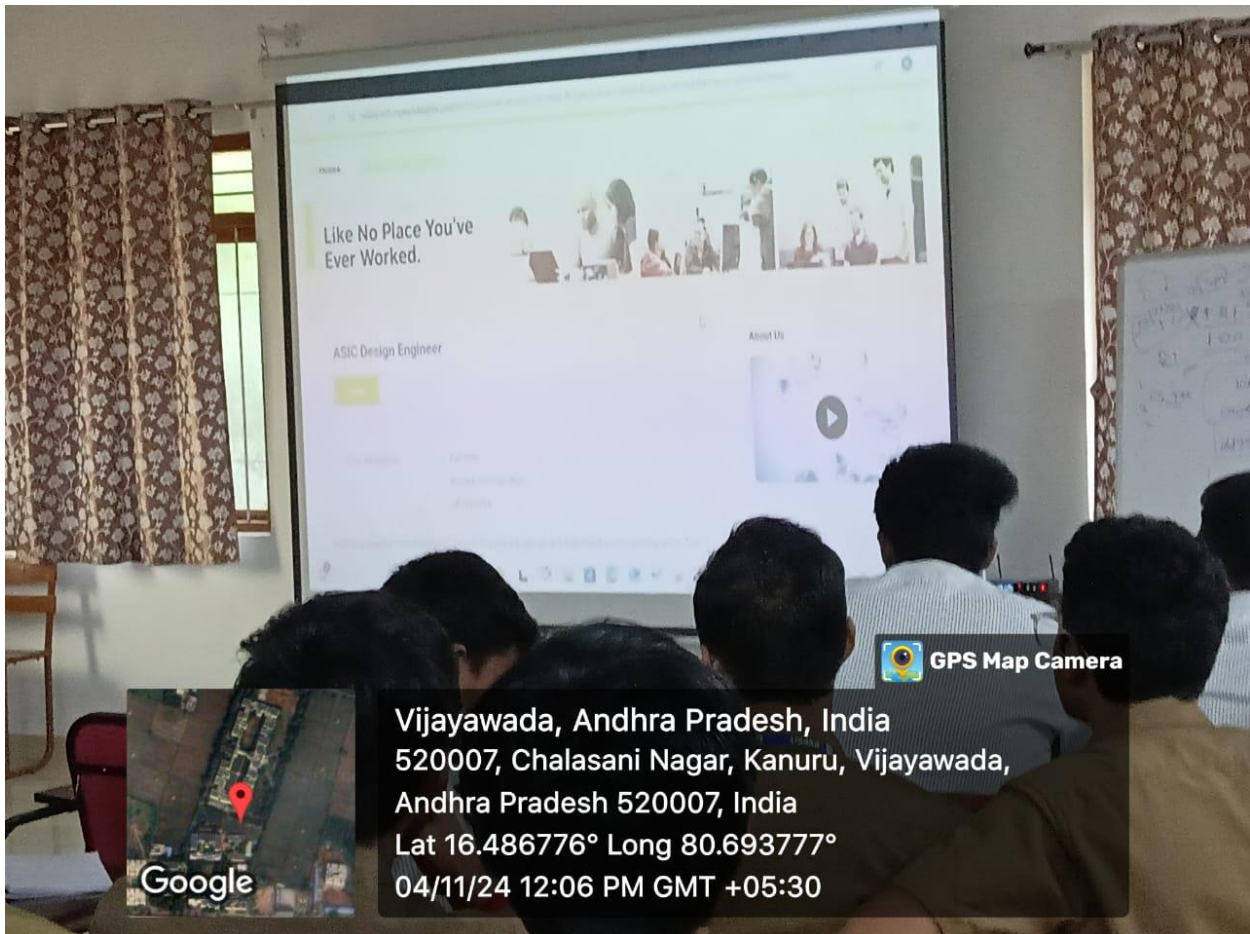
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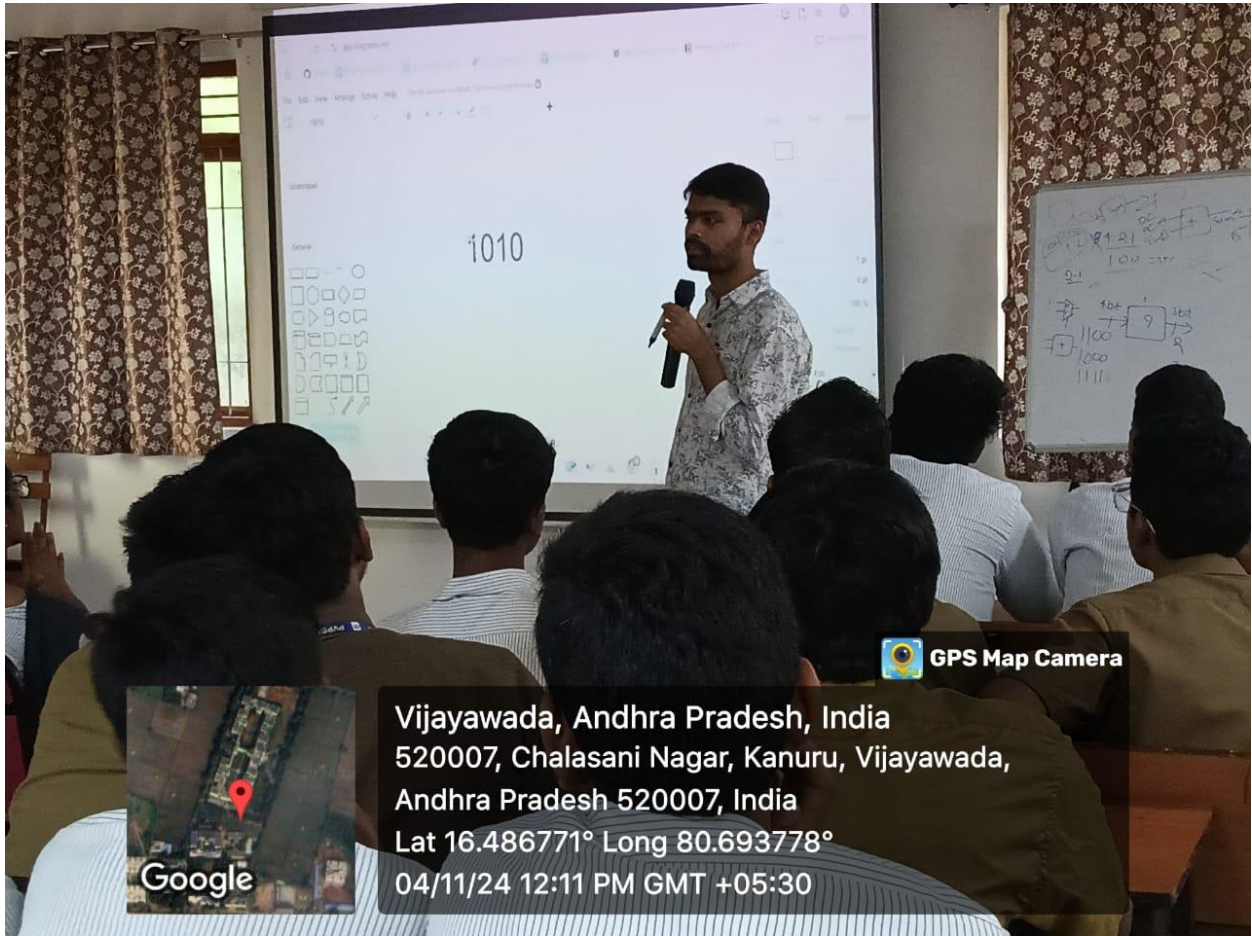
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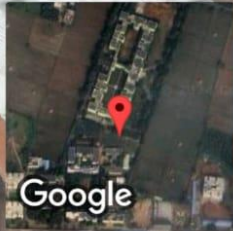
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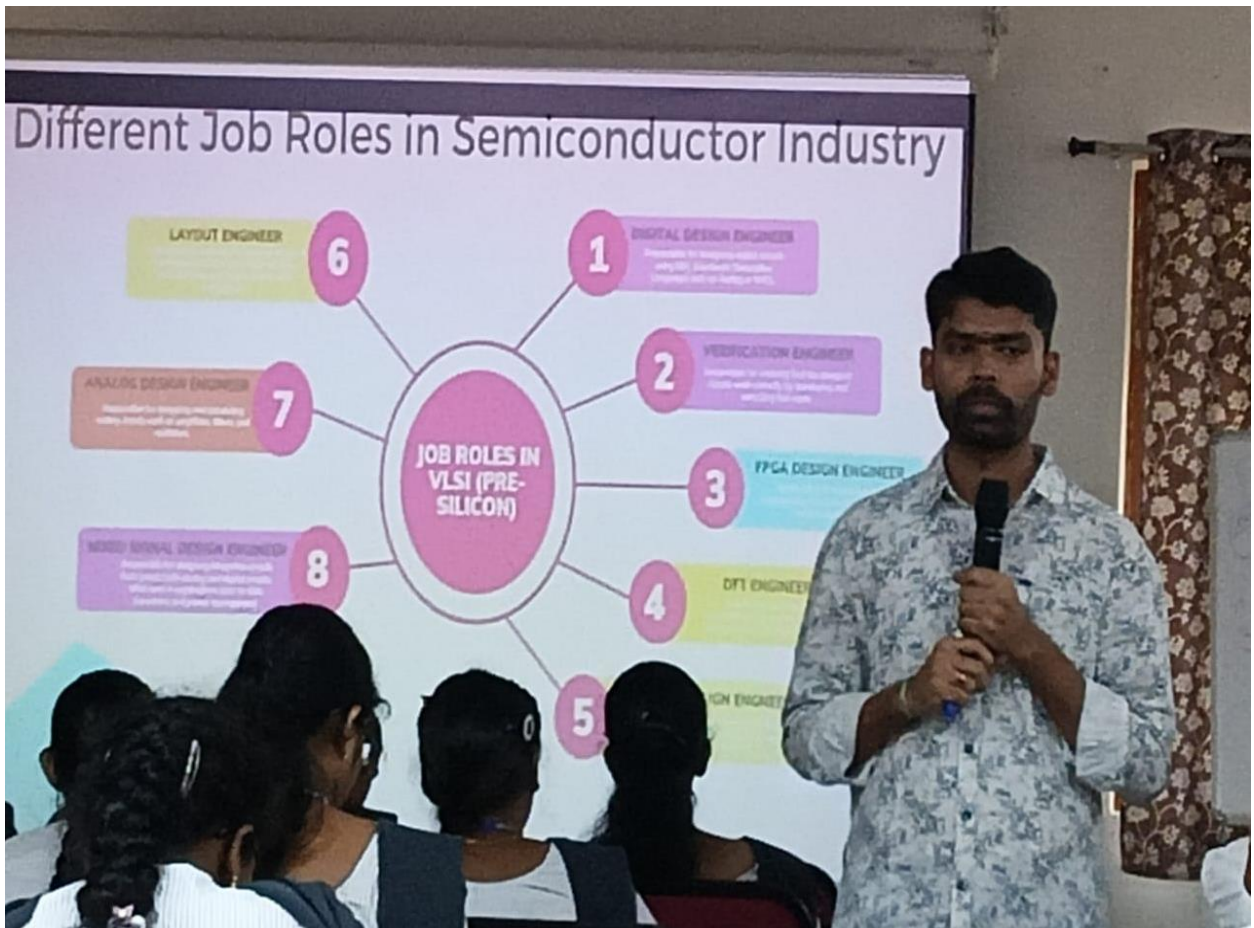
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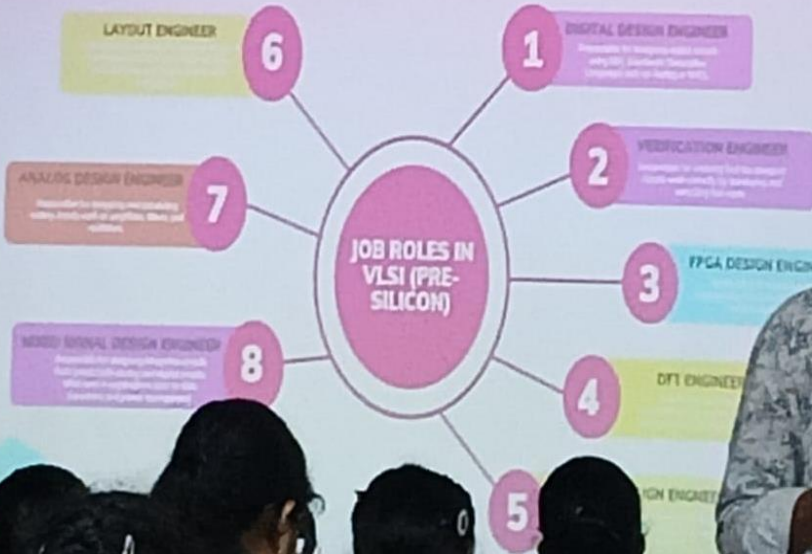
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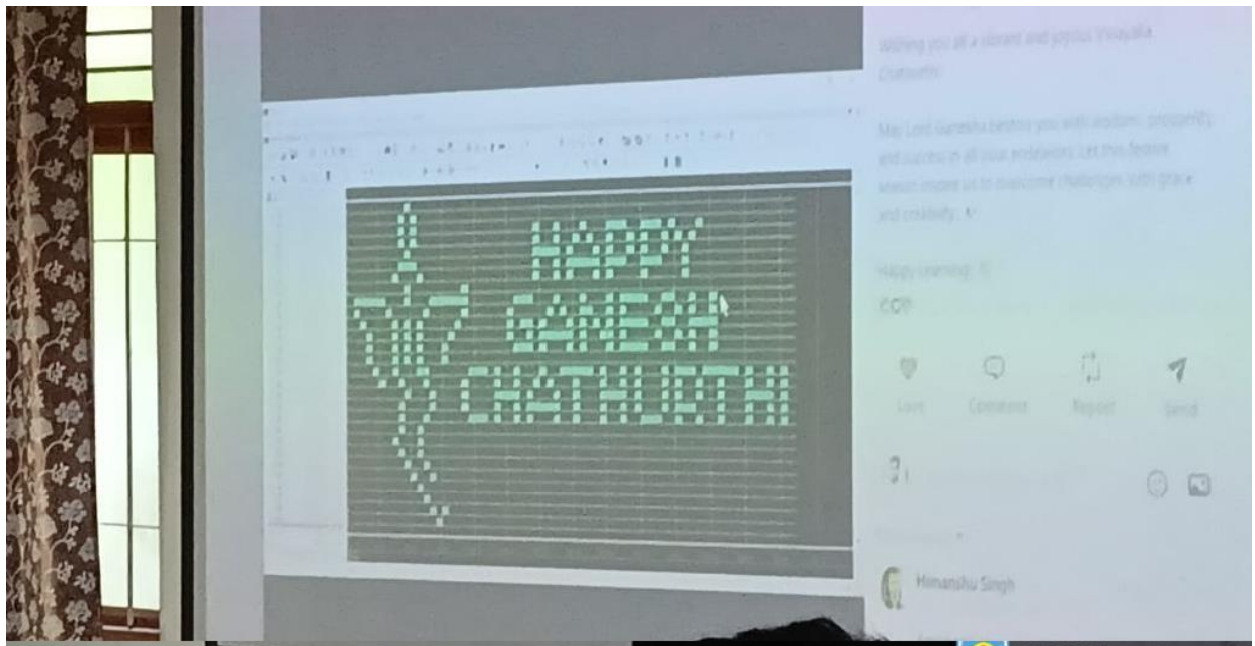


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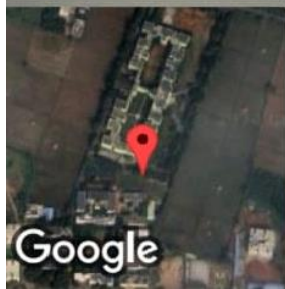


Different Job Roles in Semiconductor Industry






 GPS Map Camera

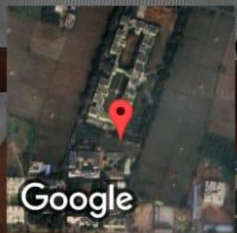


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